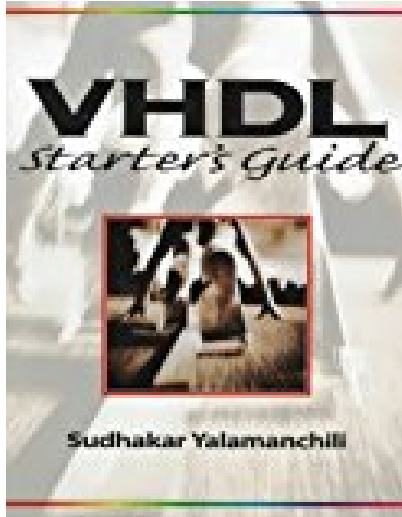


VHDL Starters Guide



BOOK DETAILS

- Author : Sudhakar Yalamanchili
- Pages : 269 Pages
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BOOK SYNOPSIS

VHDL Starters Guide has been written for the student and practitioner alike as a clear and concise tutorial on VHDL (VHSIC Hardware Description Language). It provides a hands-on, step-by-step introduction to learning VHDL as an applied language to be used in the design and testing of digital logic networks. Command syntax and structure are emphasized, and the writing is based on many examples of "real-world" logic circuits.

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